

Abstracts

A 26.5 GHz silicon MOSFET 2:1 dynamic frequency divider

M. Wetzel, L. Shi, K.A. Jenkins, P.R. de la Houssaye, Y. Taur, P.M. Asbeck and I. Lagnado. "A 26.5 GHz silicon MOSFET 2:1 dynamic frequency divider." 2000 Microwave and Guided Wave Letters 10.10 (Oct. 2000 [MGWLJ]: 421-423.

A bulk silicon divide-by-two dynamic frequency divider with maximum clock speed of 26.5 GHz has been achieved. The dynamic divider operates from 6.5 GHz to 26.5 GHz. The design is based on n-channel MOSFET's with an effective gate length of 0.1 μ m.

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